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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE (USPTO)

Serial Number	10801503
Confirmation Number	1822
Filing Date	March 15, 2004
Title of Application	A FRACTIONAL-TYPE PHASE-LOCKED LOOP CIRCUIT WITH COMPENSATION OF PHASE ERRORS
First Named Inventor	Guido Gabriele ALBASINI and Enrico Temporiti
Assignee	STMicroelectronics, S.r.l. (large entity)
Group Art Unit	2611
Examiner	Khanh C. Tran
Attorney Docket Number	2110-111-03
Nature of the Office Communication to which this is responding	Non-Final Office Action
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CERTIFICATE OF MAILING OR TRANSMISSION

I hereby certify that this correspondence is being transmitted via the Office electronic filing system, EFS-Web, addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 1st day of July, 2009

/Cindy McKee/

Signature